

REMARKS

The drawings stand objected to under 37 C.F.R. 1.83(a) for not showing the wiring traces on the adaptor board set forth in claims 1-4. By the foregoing Amendment, a revised figure is provided showing wiring traces identified by the reference numeral 29 on the adaptor board 26. Further, the specification has been amended to include the reference numeral 29. The Applicants submit that no new matter is presented by this Amendment since the application, as filed, referred to the wiring traces. Accordingly, the Applicants respectfully request withdrawal of the objection to the drawings.

Claims 1 and 2 stand rejected under 35 U.S.C. 102(b) as being anticipated by U.S. Patent No. 6,181,565 to Schmitt et al. This rejection is respectfully traversed. In the rejection, the Examiner alleges that Schmitt et al. shows a circuit card package 10 with a first circuit card 30 with a first card edge connector 30a. The Applicants would point out that the element 30a in Schmitt et al. is not a card edge connector but instead is a card edge adapted for insertion into the card edge connector 34. In fact, Schmitt et al. refers to the element 30a as an "extended portion" of the card 30 (see column 2, line 63; column 3, lines 1 and line 3; for example). Schmitt et al. discloses a conventional arrangement wherein a mother board 14 has an array of card edge connectors 34, 36 for receiving a plurality of printed circuit boards 30, 32 so that the printed circuit boards are parallel to each other and orthogonal to the mother board. In contrast, in accordance with the Applicants' claimed arrangement, there is provided a card edge connector on a first circuit card which allows an intermediate interface adaptor board to be installed orthogonal to the first circuit card. The interface adaptor board has a card edge connector thereon which allows a second circuit card to be installed parallel to the first circuit card, and the interface

adaptor board further has wiring traces interconnecting the card edge connector on the interface adaptor board with corresponding terminals of the card edge connector on the first circuit card. Schmitt et al. is totally lacking in a teaching or suggestion of the Applicants' first circuit card with a first card edge connector, since what the Examiner is referring to as the card edge connector 30a is actually a card edge, as is well known to those skilled in the art of circuit board design and construction. The Applicants' interface adaptor board can be considered to be an "intermediate" mother board which is plugged into a circuit card which in turn is plugged into a "primary" mother board. Accordingly, the Applicants submit that their Claim 1, which is the only independent claim in this application, is neither anticipated nor suggested by the teachings of Schmitt et al. Claim 2 depends from Claim 1. Accordingly, the Applicants respectfully request withdrawal of the rejection of Claims 1 and 2 under 35 U.S.C. 102(b) over Schmitt et al.

Claims 3 and 4 stand rejected under 35 U.S.C. 103(a) as being unpatentable over Schmitt et al. in view of U.S. Patent No. 3,755,630 to Boyer. This rejection is respectfully traversed. Claims 3 and 4 depend from Claim 2, which depends from 1, discussed above. The Examiner cites Boyer for its disclosure of a latch that may include a spring. However, Boyer does not make up for the deficiencies of Schmitt et al. when applied to the Applicants' independent Claim 1, as discussed above. Accordingly, the Applicants respectfully request withdrawal of the rejection of Claims 3 and 4 under 35 U.S.C. 103(a) over Schmitt et al. in view of Boyer.

In commenting on the Applicants' arguments in response to the previous Office Action, the Examiner alleged that Claims 1 and 2 clearly read on a conventional motherboard. The Applicants dispute this and submit that the interface adaptor board

described in the Applicants' specification and claims is clearly distinctively different from the conventional motherboard arrangement shown by Schmitt et al., as discussed above. The Examiner also repeats that U.S. Patent No. 6,222,725 to Jo also shows all the features of Claim 1. However, in the response to the last Office Action, the Applicants' argued over the Jo '725 Patent and apparently the Examiner agreed with the Applicants' arguments since Jo is no longer cited as a reference.

Claims 1-4 remain for consideration in this application. In view of the foregoing amendments and discussion, the Applicants submit that all of these claims are clearly allowable and favorable action in that regard is earnestly solicited. If the Examiner is of the opinion that such action cannot be taken, he is invited to telephone the undersigned at (732) 744-1001 (ext. 19) to discuss this matter without the necessity for any further Office Action.

It is believed that no additional fee is due attributable to this Amendment. Should there be any further fees required as a result of this Amendment, the Examiner is authorized to charge Deposit Account No. 19-1218.

Respectfully submitted,

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